

Notice of Allowability

Application No.

10/086,129

Applicant(s)

SCHULTZ, DAVID P.

Examiner

JAMES C. KERVEROS

Art Unit

2138

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to amendment filed 3/2/2006.
2. ☒ The allowed claim(s) is/are 1-27.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☒ Examiner's Amendment/Comment
8. ☐ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

GUY LAMARRE
PRIMARY EXAMINER

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Kim Kanzaki on April 5, 2006.

The application has been amended as follows:

CLAIM 1, the last paragraph, lines 8-14, was deleted and replaced with the following amended paragraph:

"extending an apparent length of an instruction register of said host JTAG TAP controller by using said at least one available bit from said selectable bit register, wherein the apparent length of said instruction register of said host JTAG TAP controller comprises a combination of the length of said instruction register of said host JTAG TAP controller, the length of an instruction register of said selected IP core JTAG TAP controller, and optionally, the length of the selectable bit register."

CLAIM 7, the last paragraph, lines 7-11, was deleted and replaced with the following amended paragraph:

"selecting an apparent register size of an instruction register of said host JTAG TAP controller, wherein the apparent register size of said instruction register of said

host JTAG TAP controller comprises the combined size of an instruction register for said at least one IP core JTAG TAP controller, the size of said instruction register of said host JTAG TAP controller, and optionally, the size of the selectable bit register.”

CLAIM 11, the last paragraph, lines 5-11, was deleted and replaced with the following amended paragraph:

“a selector for selecting said at least one available bit, said selector extending an apparent length of an instruction register of said host JTAG TAP controller coupled in series with an IP core JTAG TAP controller by using said selected at least one available bit from said selectable bit register, wherein the apparent length of said instruction register comprises a combination of a length of an instruction register of said IP core JTAG TAP controller, a length of said instruction register of said host JTAG TAP controller, and optionally, the length of the selectable bit register.”

CLAIM 14, the last paragraph, lines 7-12, was deleted and replaced with the following amended paragraph:

“an instruction register size select signal enabling the selection of an apparent register size of an instruction register of said host JTAG TAP controller, wherein the apparent register size of said instruction register of said host JTAG TAP controller comprises a combination of a size of an instruction register of said at least one IP core JTAG TAP controller, a size of said instruction register of said host JTAG TAP controller, and optionally, the size of the selectable bit register.”

CLAIM 21, the last paragraph, lines 5-11, was deleted and replaced with the following amended paragraph:

“a host JTAG TAP controller coupled to each of the first JTAG TAP controllers, said host JTAG TAP controller comprising a selectable bit register enabling the selection of an apparent register size of an instruction register of the host JTAG TAP controller, wherein the apparent register size of the instruction register of the host JTAG TAP controller comprises a combination of a size of an instruction register of the first JTAG TAP controller for each IP core of the plurality of IP cores, the size of the instruction register of the host JTAG TAP controller, and optionally, the size of the selectable bit register.”

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to JAMES C. KERVEROS whose telephone number is (571) 272-3824. The examiner can normally be reached on 9:00 AM TO 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert Decady can be reached on (571) 272-3819. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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JAMES C KERVEROS
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By:  4/5/06



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